

Preliminary Amendment  
National Stage Entry of PCT/JP2003/15468  
Q88131

**Please delete the present Abstract of the Disclosure.**

**Please add the following new Abstract of the Disclosure:**

The present invention provides an amplifying circuit capable of accomplishing high-impedance input/output, and providing a high gain ~~in~~<sup>and</sup> low power consumption. The amplifier 208 amplifies a signal received through an input terminal ~~IN~~, and outputs the signal through an output terminal ~~OUT~~. A control circuit comprised of the inductors 203 and 204, and the switches 209 and 210 turns input/output impedances of the amplifier 208 into a high impedance.